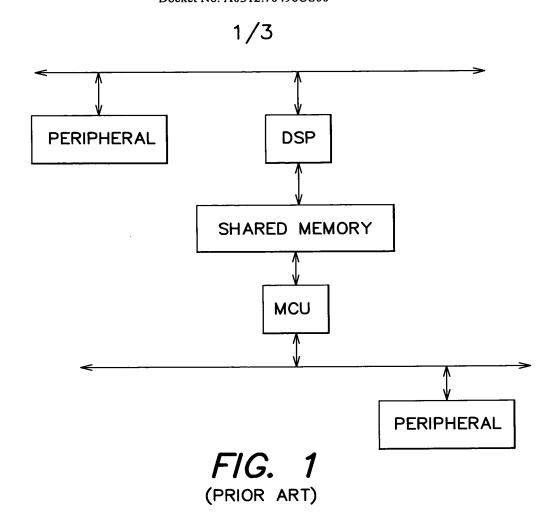
BUS ARCHITECTURE AND SHARED BUS ARBITRATION METHOD FOR A COMMUNICATION PROCESSOR, Rainer R. Hadwiger et al. Docket No. A0312.70496US00



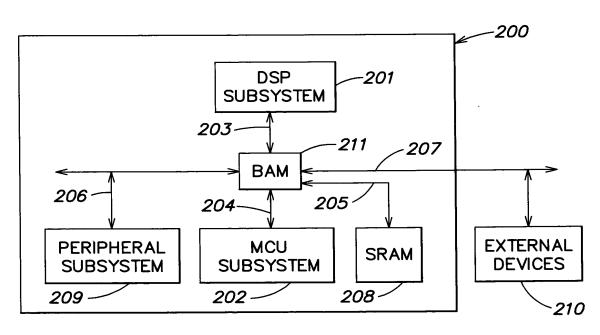
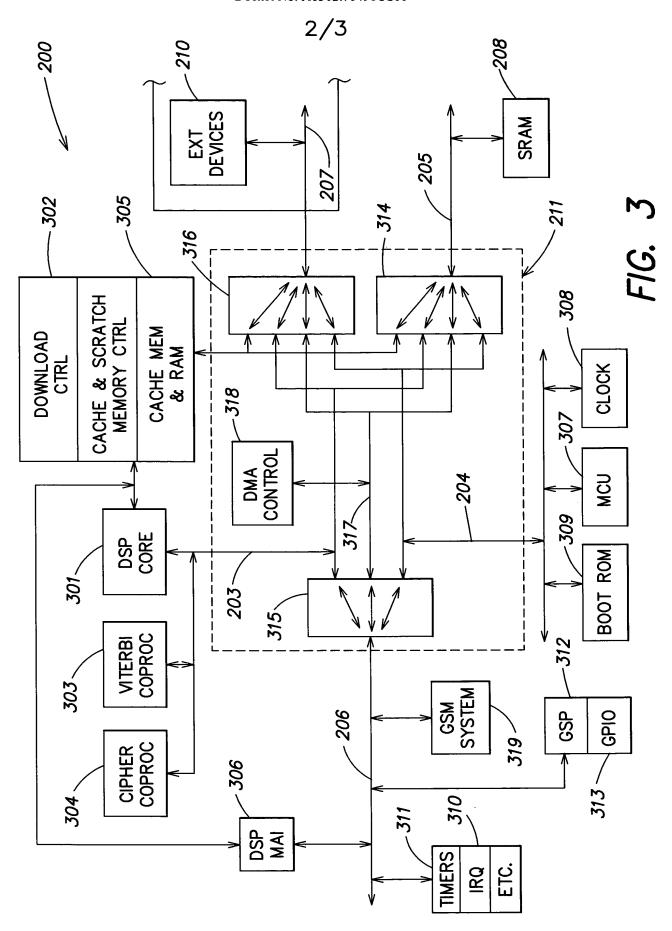


FIG. 2

BUS ARCHITECTURE AND SHARED BUS ARBITRATION METHOD FOR A COMMUNICATION PROCESSOR, Rainer R. Hadwiger et al. Docket No. A0312.70496US00



BUS ARCHITECTURE AND SHARED BUS ARBITRATION METHOD FOR A COMMUNICATION PROCESSOR, Rainer R. Hadwiger et al. Docket No. A0312.70496US00

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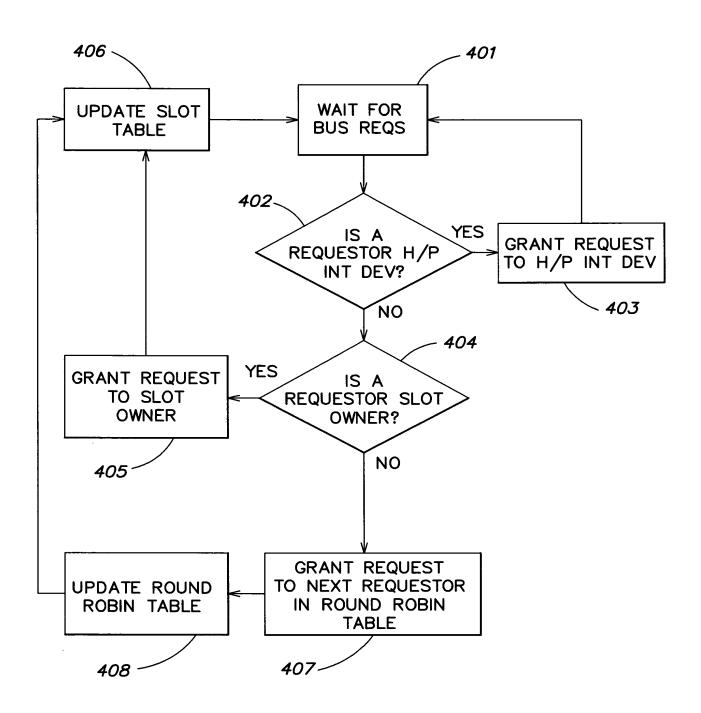


FIG. 4